

IN THE CLAIMS

Please amend the claims as follows.

1-12. (Canceled)

13. (Currently Amended) The system of claim [[12]] 36 further including a capacitor between the primary output terminal and the primary input terminal.

14. (Currently Amended) The system of claim [[12]] 36 further including a primary filter coupled between the first primary feedback input and the primary feedback output.

15. (Original) The system of claim 14 wherein the primary filter includes a capacitor.

16. (Currently Amended) The system of claim [[12]] 36 wherein the primary feedback circuit includes a resistor.

17. (Currently Amended) The system of claim [[12]] 36 wherein the primary feedback circuit includes a transistor.

18. (Currently Amended) The system of claim [[12]] 36 wherein the primary input terminal is coupled to the primary output terminal by a cable.

19. (Currently Amended) The system of claim [[12]] 36 wherein the primary input terminal is coupled to the primary external output terminal by a backplane.

20 – 35. (Canceled)

36. (Previously Presented) A system comprising:

a driver having a primary output terminal and a secondary output terminal;

a receiver having a primary input terminal coupled to the primary output terminal and a secondary input terminal coupled to the secondary output terminal;

a primary peak detector coupled to the primary input terminal and having a primary peak output;

an output amplifier having a first amplifier input coupled to the primary input terminal and a second amplifier input coupled to a first reference potential;

a primary feedback amplifier having a first primary feedback input coupled to the primary peak output and a second primary feedback input coupled to a second reference potential and having a primary feedback output;

a primary feedback circuit coupled to the primary feedback output and coupled to the primary input terminal; and

a secondary peak detector coupled to the secondary input terminal and having a secondary peak output;

a secondary feedback amplifier having a first secondary feedback input coupled to the secondary peak output and a second secondary feedback input coupled to the primary peak output and having a secondary feedback output; and

a secondary feedback circuit coupled to the secondary feedback output and coupled to the secondary input terminal; and

wherein the second amplifier input is coupled to the secondary input terminal.